

Claims

- [c1] 1. A liquid crystal display panel, comprising:
a first substrate, wherein the first substrate has an array area and a pair of non-display areas opposite positioned on each side of the array area, and the non-display areas furthermore have a plurality of driver chip bonding areas therein;
a second substrate formed over the array area;
a liquid crystal layer formed between the first substrate and the second substrate; and
a plurality of driver chips formed on the driver chip bonding areas for driving the liquid crystal layer above the driver array area.
- [c2] 2. The liquid crystal display panel of claim 1, wherein the non-display areas furthermore comprise a plurality of flexible printed circuit film bonding areas for connecting electrically with flexible printed circuit films.
- [c3] 3. The liquid crystal display panel of claim 1, wherein the first substrate comprises an active matrix array substrate.
- [c4] 4. The liquid crystal display panel of claim 3, wherein the

active matrix array substrate furthermore comprises a thin film transistor array substrate.

- [c5] 5. The liquid crystal display panel of claim 1, wherein the first substrate comprises a passive matrix array substrate.
- [c6] 6. The liquid crystal display panel of claim 1, wherein the second substrate comprises a color-filter substrate.
- [c7] 7. The liquid crystal display panel of claim 1, wherein the driver chips comprise gate driver chips and source driver chips.
- [c8] 8. A liquid crystal display panel having a display area and a pair of non-display areas opposite positioned on each side of the display area.
- [c9] 9. The liquid crystal display panel of claim 8, wherein the non-display areas furthermore comprise a plurality of driver chip bonding areas for connecting electrically with a plurality of driver chips.
- [c10] 10. The liquid crystal display panel of claim 9, wherein the driver chips comprise gate driver chips and source driver chips.
- [c11] 11. The liquid crystal display panel of claim 8, wherein the non-display areas furthermore comprise a plurality

of flexible printed circuit film bonding areas for connecting electrically with a plurality of flexible printed circuit films.